The conference program will include keynote speeches, contributed paper sessions, and demonstrations. DASIP 2010 will feature Special Sessions with the purpose of introducing the DASIP community to relevant hot topics that were not sufficiently covered by previous editions of the conference.

**Five Special Sessions are planned on the topics of the conference:**
- Reliable multi-processor scheduling and HW/SW Resource Management
- Reconfigurable computing architectures
- Image and signal processing on GPU
- Advances in reconfigurable video coding (RVC)
- Smart image sensors

Authors should submit their full papers (up to 8 pages, double-column IEEE format) in PDF through the web based submission system. Proceedings of DASIP 2010 will be included in the IEEE Xplore Digital Library. Authors of the best papers will be invited to submit an extended version of their work to the International Journal of Embedded and Real-Time Communication Systems (IJERTCS), in which a special issue on DASIP will be published on the third quarter of 2011.